

**King Saud University**  
**College of Engineering**  
**Electrical Engineering Dept**  
**EE201 Fundamentals of Electric Circuits**

**Second Semester 1435/1436**

**Instructor:**

Dr. Nacer A. Debbar, Office: 2C-24/1, [debbar@ksu.edu.sa](mailto:debbar@ksu.edu.sa)  
Phone: 467-7818  
Dr. Abdulhameed A. Al-Ohaly, Office: 2C-124, [aohaly@ksu.edu.sa](mailto:aohaly@ksu.edu.sa)  
Phone: 467-76253  
Dr. Mamdooh S. Al-Saud, Office: 2C-43, [mamdooh@ksu.edu.sa](mailto:mamdooh@ksu.edu.sa)  
Phone: 467-0592  
Dr. Ehab Awad, Office: 2C 91-2, [eawad@ksu.edu.sa](mailto:eawad@ksu.edu.sa)

**Office hours:** TBA

**Text Book:**

“Introductory Circuit Analysis” By Robert L. Boylestad, 12<sup>th</sup> (or 11<sup>th</sup> or 10<sup>th</sup>) Edition, Published by Prentice Hall, 2001.

**Course outline:**

<b>Topic</b>	<b>Chapters</b>
Definitions and Laws	1-4
Series/Parallel (DC) circuits analysis	5-8
Network Theorems (DC) circuits	9
Sinusoidal alternating and phasors	13-14
Series/Parallel (AC) circuits analysis	15-17
Network Theorems (AC) circuits	18
Power (AC)	19
Polyphase Systems	22

**Grading Policy:**

Mid-Term Exams: 20% + 20%  
Home Works + Quizzes 20%  
Final Exam 40%

**Mid-term Exams:**

First mid-term Exam: Sunday, 5 April 2015  
Second Mid-Term Exam: Sunday, 3 May 2015

**Notes:**

1. All mid-term exams will be performed after Maghreb prayers
2. If you miss any mid-term exam, there will be no makeup test for any given reasons
3. The only allowed calculator during Mid-term and Final exams is “**Casio FX-991ES**” or “**Casio FX-991ES Plus**”.

**Attendance Policy:**

Attendance will be taken at every lecture. Students with less than 75% attendance will be forbidden from entering the final exam. In addition, all students who are late more than five minutes for the lectures will not be allowed to enter the classroom.

**Cheating Policy:**

Cheating and plagiarism of any kind will not be tolerated. This includes giving answers as well as taking them. This applies to any course work, tests, quizzes and homework. Each person’s answers to an assignment should be his alone and should not be identical to another student’s work. Cheating will result in a grade of "F" for all persons involved, if convicted.

**Weekly Teaching Plan:**

Subjects	Week
<b>Chapter 2- Current and Voltage</b> 2.2 Current 2.3 Voltage 2.4 Fixed dc supplies <b>Chapter 4- Ohm’s Law, Power and Energy</b> 4.1 Ohm’s law 4.3 Power <b>Chapter 5- Series Circuit</b> 5.2 Series circuits 5.3 Voltage sources in series 5.4 Kirchhoff’s voltage law 5.5 Voltage divider rule 5.6 Notation	2
<b>Chapter 6- Parallel Circuits</b> 6.2 Parallel elements 6.3 Total conductance and resistance 6.4 Parallel networks 6.5 Kirchhoff’s current law 6.6 Current divider rule 6.7 Voltage sources in parallel 6.8 Open and short circuits	3
<b>Chapter 7- Series- Parallel Networks</b> 7.1 Series-Parallel networks 7.2 Descriptive Examples 7.3 Ladder networks	4

<b>Chapter 8- Methods of Analysis and Selected Topics (dc) 5</b> 8.2 Current sources 8.3 Source conversions 8.4 Current sources in parallel 8.5 Current sources in series 8.7 Mesh analysis (general approach) 8.8 Mesh analysis (formatted approach)	5
<b>Chapter 8- Methods of Analysis and Selected Topics (dc)</b> 8.9 Nodal analysis (general approach) 8.10 Nodal analysis (formatted approach) 8.11 Bridge network 8.12 Star- Delta conversions	6
<b>Chapter 9- Network Theorems</b> 9.2 Superposition theorem 9.3 Thevenin's theorem	7
<b>Chapter 9- Network Theorems</b> 9.4 Norton's theorem 9.5 Maximum power transfer Theorem <b>Chapter 13- Sinusoidal Alternating Waveforms</b> 13.2 AC Voltage Definition 13.4 General format for the sinusoidal V or I 13.5 Phase relation 13.7 Effective value	8,9
<b>Chapter 14- The basic Elements and Phasors</b> 14.3 Response of basic R, L, and C elements to a sinusoidal V or I 14.5 Average power and power factor 14.6 Complex numbers	10
<b>Chapter 14- The basic Elements and Phasors</b> 14.1 Rectangular form 14.2 Polar form 14.3 Conversion between forms 14.4 Mathematical operations with complex numbers 14.5 Phasors <b>Chapter 15- Series and Parallel ac Circuits</b> 15.2 Impedance and the phasor diagram 15.3 Series configuration	11
<b>Chapter 15- Series and Parallel ac Circuits</b> 15.4 Voltage divider rule 15.6 Admittance and susceptance 15.7 Basic elements in parallel ac networks	12

15.8 Current divider rule 15.9 Equivalent circuits <b>Chapter 16- Series-Parallel ac Networks</b> 16.1 Introduction 16.2 Illustrative example 16.3 Ladder networks	
<b>Chapter 17- Methods of Analysis and Selected Topics (ac)</b> 17.3 Source conversions 17.4 Mesh analysis (formatted approach) 17.5 Nodal analysis (formatted approach) 17.6 Bridge networks (ac) 17.7 Star-Delta conversions <b>Chapter 18- Network Theorems</b> Same theorems as in Chapter 9	13
<b>Chapter 19- Power (ac)</b> 19-3 Apparent Power 19-4 Inductive circuit and reactive power (Q) 19-5 Capacitive circuit 19-6 The power triangle 19-7 The total P, Q, and S 19-8 Power factor correction	14
<b>Chapter 22- Polyphase Systems</b> 22-2 The three-phase generator 22.3 The Y-connected generator 22.4 Phase sequence (Y-connected Generator) 22.6 The Y- $\Delta$ system 22.10 Three-phase POWER	15