

Sample Exam

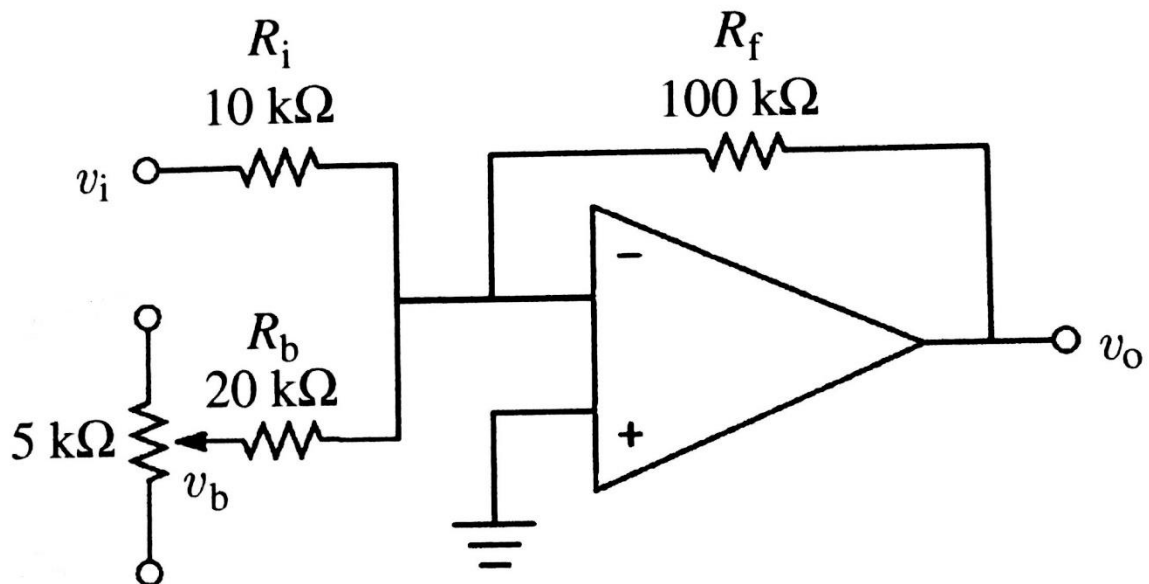
Inverting Amplifier

Q1: A biopotential preamplifier, used in an electromyogram circuit, has an undesired dc voltage of ± 5 V due to unwanted interference, while the desired signal is ± 1 V superimposed.

Design a circuit that will balance the dc voltage to zero and a gain of -10 for the desired signal without saturating the op amp.

Answer:

The figure below shows the proposed design. We assume that v_b , the balancing voltage available from the 5 k Ω potentiometer, is ± 10 V.



The undesired voltage at $v_i = 5$ V. For $v_o = 0$ V, the current flowing through R_f should be zero.

Therefore, the sum of the currents through R_i and R_b is zero.

$$\frac{v_i}{R_i} + \frac{v_b}{R_b} = 0$$

$$R_b = R_i \frac{v_b}{v_i} = -\frac{10^4(-10)}{5}$$

$$R_b = 2 \times 10^4 \Omega$$

To obtain a gain of -10, R_f/R_i should equal 10 (i.e. $R_f = 100 \text{ k}\Omega$)

$$v_o = -R_f \left(\frac{v_i}{R_i} + \frac{v_b}{R_b} \right)$$

$$v_o = -10^5 \left(\frac{v_i}{10^4} + \frac{v_b}{2 \times 10^4} \right)$$

$$v_o = -10 \left(v_i + \frac{v_b}{2} \right)$$

One Op-Amp Differential Amplifier

Q2: A blood pressure uses a strain gage to monitor the blood pressure. The strain gage is composed of four active arms of Wheatstone bridge. This bridge is excited with 5 V dc, and each arm changes resistance by $\pm 0.3\%$.

Design an amplifier that will provide a full-scale out over the op-amp's full range of linear operation, using the minimal number of components.

Answer:

$$\text{The change in } v_o \text{ of the bridge} = v_i \frac{\Delta R}{R}$$

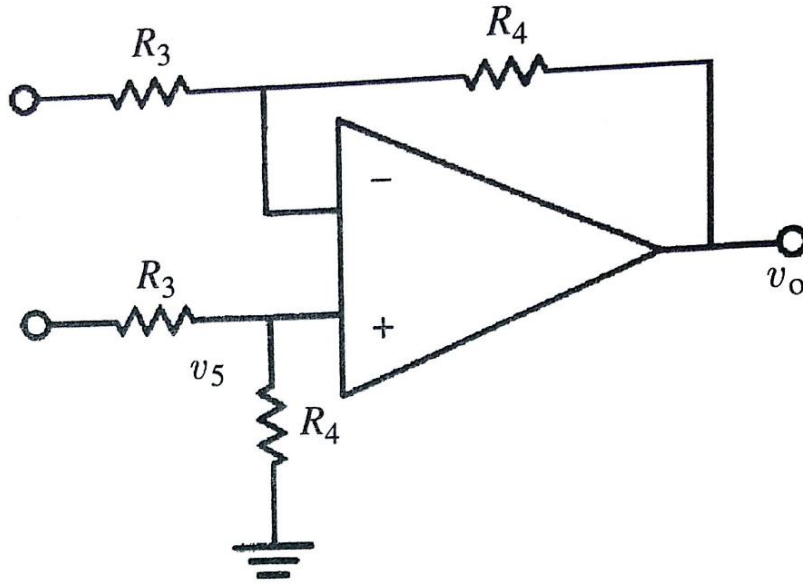
$$= (5 \text{ V}) 0.003 = 0.015 \text{ V}$$

$$\text{Gain} = 20/0.015 = 1333$$

To calculate the resistors values of the differential op-amp:

$$\text{Assume } R = 120 \Omega$$

Then, the Thevenin source impedance = 60Ω



We will use this value to replace R_3 (in the shown circuit).

Since the gain = R_4/R_3

$$R_4 = (1333) R_3$$

$$R_4 = 80 \text{ k}\Omega$$

Filters

Q3: Explain how operational amplifiers could be used as low pass filter, emphasizing on the basic components' properties that facilitate this application.

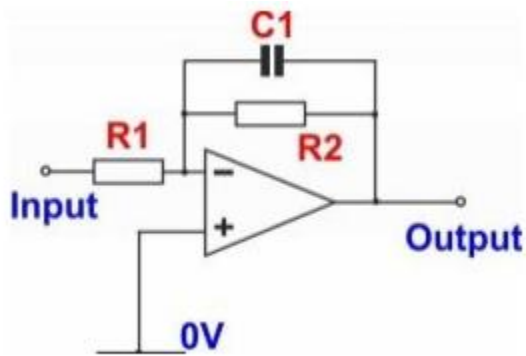
Draw the low pass filter circuit, and derive the mathematical equation of the low pass filter.

Answer:

In the low pass filter circuitry, a capacitor is placed, in the feedback, across the resistor. As the frequency of input signal increases, the reactive impedance of the capacitor falls. This decreases the resultant impedance of the feedback loop, resulting in lower gain at higher

frequencies. Conversely, the amplifier gain is still high at lower frequencies. That allows the amplifier to pass low frequencies while blocking higher ones.

Low pass filter circuit



$$X_c = \frac{1}{2\pi f c}$$

Where:

Xc is the capacitive reactance in ohms

π is 3.14

f is frequency in Hertz

C is the capacitance in Farads

Amplifier gain = Z_{out}/Z_{in}

$$z_{out} = \frac{\frac{R_2}{2\pi f c}}{\frac{1}{2\pi f c} + R_2}, \text{ and } Z_{in} = R_1$$

$$\text{Low pass filter gain} = \frac{R_2}{R_1} \left(\frac{1}{1 + 2\pi f c R_2} \right)$$