

**King Saud University
College of Engineering
Electrical Engineering Department**

**EE208 Logic Design
First Semester 1428/1429**

Instructor: Prof. Adnan Nouh
Office 2C32 Bld. 3
Phone: 4676817
E mail : asnouh ksu.edu.sa

Textbook: Digital Design by M. Morris Mano, Prentice Hall, Latest edition

Course outline:

1- Number Systems	Chapter 1
2-Boolean Algebra & Logic gates	Chapter 2
3-Boolean functions and simplification	Chapter 3
4- Combinational logic circuits	Chapter 4
5- MSI & PLD Components	Chapter 5
6-Sequential circuits	Chapter 6
7-Registers, counters & memory units	Chapter 7

Evaluation:

10% Section work
20% First mid-term exam. (7th week)
20% Second mid-term exam. (12th week)
10% Quiz & Attendance
40% Final Exam.

Attendance policy:

According to KSU policy, every student should attend at least 75% of the course classes (including the tutorials). Those who fail to fulfil this condition will fail the course