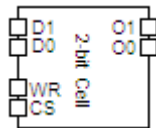


King Saud University
College Of Computer & Information Sciences
Department Of Computer Sciences
CSC - 220 (Fall - 2010)
Lab # 4 (Random Access Memory)

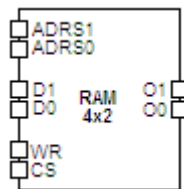
Question # 1

Design the logic circuit of the hardware that implements a 2-bit RAM Cell



Question # 2

Using the design implemented in Question 1 as macro; design the logic diagram of the hardware that implements a 4x2 RAM.



Question # 3

Using the design implemented in Question 2 as macro; design the logic diagram of the hardware that implements the following RAM transfer statement:

yT1: M[01] <----- 10